

Technological Fabrication Features of Microwave Device with Schottky Barriers

V. S. Dmitriev, L. B. Dmitrieva*, and E. Ya. Shvets
Zaporozhye State Engineering Academy, Zaporizhzhya, Ukraine
e-mail: ktn.dmitrieva@gmail.com

Received in final form December 19, 2017

Abstract—At present, research and development of heterojunctions are conducted in the directions of searching for new compositions and technological regimes for the creation of ohmic and barrier transitions for gallium arsenide. The transition to silver-based metallization, which has large thermal and electrical conductivity comparing with gold and a relatively low diffusion coefficient to gallium arsenide, should improve the technical characteristics of the devices. One of the most important technological operations in the formation of Schottky ohmic contacts and barriers is thermal annealing. Silver to gallium arsenide contacts are made in vacuum by the method of thermal evaporation. The deposition and thermal treatment regimes for creating ohmic contacts of Ag–Ge–In/ n - n^+ GaAs with specific contact resistance $\rho_c = (5..7) \times 10^{-5} \Omega \cdot \text{cm}^2$ are developed. The influence of the substrate temperature during the silver deposition and the annealing temperature on the height of the Schottky barrier Ag/ n - n^+ GaAs, the injection coefficient γ and the nonideality factor η is established.

DOI: 10.3103/S073527271802005X

INTRODUCTION

The development of mobile communications and tablet devices, the use of high-speed wireless telecommunications, the increase in the capacity of fiber-optic systems that requires an increase in the data transfer rate, necessitates a sharp increase in the share of microwave electronic devices [1–6], which are made on the basis of electronic gallium arsenide.

At present, research and development of heterojunctions are conducted in the search for new metal-gallium arsenide compositions [7–9], the development of technological regimes [8–10], which can ensure the reproducibility of the parameters of microwave devices with Schottky barriers (SB).

Despite the fact that the key physical processes in heterojunctions of metal-gallium arsenide have been thoroughly studied [10–12], there are still disagreements about the reasons for deviation of current–voltage characteristics from ideal ones [13–16], which is related to the technological features of their fabrication.

At present, the most advanced technologies for fabricating metal–gallium arsenide heterojunctions are based on gold [13–16]. However, the transition to silver-based metallization, which has a large thermal and electrical conductivity compared with gold and a relatively low diffusion coefficient to gallium arsenide [10–12], should improve the technical characteristics of the devices. Therefore, the development of technological regimes for manufacturing ohmic and barrier transitions to Ag-based GaAs is relevant from the scientific and practical point of view.

PROBLEM STATEMENT

The main problems of metal-gallium arsenide heterojunctions [7, 9, 10, 12, 18–20] are: the absence of uniform wetting of the metal, surface roughness, segregation of metal into the contact region, the presence of multiple metal–semiconductor phases, erosion of the contact surface and low thermal stability of contact electrical parameters.

During the fabrication process of microwave semiconductor devices and integrated microcircuits based on gallium arsenide, one of the most important technological operations is thermal annealing in the process of formation of ohmic contacts (OC) [21, 22] and Schottky barriers [23, 24].

REFERENCES

1. A. I. Belous, V. A. Solodukha, S. V. Shvedov, *Space Electronics*, 2nd book [in Russian] (Tekhnosfera, Moscow, 2015).
2. E. Ya. Shvets, A. G. Kolomoets, "Estimation of prospects of application of GaAs and alloys on his basis as materials for sun elements," *Metallurgy*, No. 30, 132 (2013). URI: http://www.zgia.zp.ua/gazeta/Metallurgy_30_132.pdf.
3. M. V. Zagirniak, A. P. Oksanich, V. R. Petrenko, S. E. Pritchyn, V. A. Terban, "Development of modern technologies for growing structurally perfect ingots of electronic gallium arsenide," *Proc. of 5th Int. Sci. Conf. on Functional Base of Nanoelectronics*, Katsiveli (NURE, Kharkiv, 2012), pp. 5-13.
4. S. A. Zuev, G. V. Kilessa, E. E. Asanov, V. V. Starostenko, S. V. Pokrova, "Dependence of the conductivity on the active-region thickness in GaAs thin-film Schottky diodes," *Semiconductors* **50**, No. 6, 810 (2016). DOI: [10.1134/S1063782616060269](https://doi.org/10.1134/S1063782616060269).
5. Chan Hyeong Park, Jong-Ho Lee, "Formulas of 1/f noise in Schottky barrier diodes under reverse bias," *Solid-State Electronics* **69**, 85 (2012). DOI: [10.1016/j.sse.2011.11.030](https://doi.org/10.1016/j.sse.2011.11.030).
6. S. V. Platonov, N. V. Permyakov, B. I. Seleznev, V. A. Moshnikov, E. Yu. Kozlovskiy, A. M. Osipov, "Low-noise gallium-arsenide amplifiers under the influence of electromagnetic interferences of increased intensity," *Bulletin of Novgorod State University*, No. 67, 29 (2012). URI: <http://www.novsu.ru/file/1010219>.
7. E. V. Erofeev, "Formation of metal-semiconductor contacts with metallization on the basis of Al and Cu for GaAs microwave transistors with high electron mobility," PhD thesis, specialization: 01.04.04 Physical electronics. (Tomsk, 2012). URI: <http://old.tusur.ru/export/sites/ru.tusur.new/ru/science/education/diss/2012/03/01.pdf>.
8. G. I. Koltsov, S. I. Didenko, A. V. Chernykh, S. V. Chernykh, A. P. Chubenko, Yu. N. Sveshnikov, "Schottky contacts to high-resistivity epitaxial GaAs layers for detectors of particles and X- or γ -ray photons," *Semiconductors* **46**, No. 8, 1066 (2012). DOI: [10.1134/S106378261208009X](https://doi.org/10.1134/S106378261208009X).
9. H. Tecimer, A. Türüt, H. Uslu, Ş. Altındal, İ. Uslu, "Temperature dependent current-transport mechanism in Au/(Zn-doped)PVA/n-GaAs Schottky barrier diodes (SBDs)," *Sensors and Actuators A: Physical* **199**, 194 (2013). DOI: [10.1016/j.sna.2013.05.027](https://doi.org/10.1016/j.sna.2013.05.027).
10. P. Jayavel, J. Kumar, P. Ramasamy, R. Premanand, "On the evaluation of Schottky barrier diode parameters of Pd, Au and Ag/n-GaAs," *Indian J. Eng. Materials Sci.* **7**, No. 5-6, 340 (2000). URI: <http://nopr.niscair.res.in/handle/123456789/24425>.
11. V. S. Dmitriev, E. Ya. Shvets, "Technological features of manufacturing a traveling wave amplifier," *Proc. of 10th Int. Youth Sci. Conf. on Modern Problems of Radio Engineering and Telecommunications*, RT-2014, Sevastopol (SevNTU, 2014). p. 158. ISBN 978-617-612-072.
12. P. Huo, I. Rey-Stolle, "Ti/Pd/Ag contacts to n-type GaAs for high current density devices," *J. Electronic Materials* **45**, No. 6, 2769 (2016). DOI: [10.1007/s11664-016-4432-6](https://doi.org/10.1007/s11664-016-4432-6).
13. E. Özavcı, S. Demirezen, U. Aydemir, Ş. Altındal, "A detailed study on current-voltage characteristics of Au/n-GaAs in wide temperature range," *Sensors and Actuators A: Physical* **194**, 259 (2013). DOI: [10.1016/j.sna.2013.02.018](https://doi.org/10.1016/j.sna.2013.02.018).
14. M. K. Hudait, P. Venkateswarlu, S. B. Krupanidhi, "Electrical transport characteristics of Au/n-GaAs Schottky diodes on n-Ge at low temperatures," *Solid-State Electronics* **45**, No. 1, 133 (2001). DOI: [10.1016/S0038-1101\(00\)00230-6](https://doi.org/10.1016/S0038-1101(00)00230-6).
15. D. Korucu, A. Turut, Ş. Altındal, "The origin of negative capacitance in Au/n-GaAs Schottky barrier diodes (SBDs) prepared by photolithography technique in the wide frequency range," *Current Appl. Phys.* **13**, No. 6, 1101 (2013). DOI: [10.1016/j.cap.2013.03.001](https://doi.org/10.1016/j.cap.2013.03.001).
16. W. P. Leroy, K. Opsomer, S. Forment, R. L. Van Meirhaeghe, "The barrier height inhomogeneity in identically prepared Au/n-GaAs Schottky barrier diodes," *Solid-State Electronics* **49**, No. 6, 878 (2005). DOI: [10.1016/j.sse.2005.03.005](https://doi.org/10.1016/j.sse.2005.03.005).
17. Jing Lv, Fachun Lai, Limei Lin, Yongzhong Lin, Zhigao Huang, Rong Chen, "Thermal stability of Ag films in air prepared by thermal evaporation," *Appl. Surface Sci.* **253**, No. 17, 7036 (2007). DOI: [10.1016/j.apsusc.2007.02.058](https://doi.org/10.1016/j.apsusc.2007.02.058).
18. H. C. Kim, T. L. Alford, "Improvement of the thermal stability of silver metallization," *J. Appl. Phys.* **94**, No. 8, 5393 (2003). DOI: [10.1063/1.1609646](https://doi.org/10.1063/1.1609646).
19. K. Sugawara, M. Kawamura, Y. Abe, K. Sasaki, "Comparison of the agglomeration behavior of Ag(Al) films and Ag(Au) films," *Microelectron. Eng.* **84**, No. 11, 2476 (2007). DOI: [10.1016/j.mee.2007.05.050](https://doi.org/10.1016/j.mee.2007.05.050).
20. M. Kawamura, M. Yamaguchi, Y. Abe, K. Sasaki, "Electrical and morphological change of Ag-Ni films by annealing in vacuum," *Microelectron. Eng.* **82**, No. 3-4, 277 (2005). DOI: [10.1016/j.mee.2005.07.035](https://doi.org/10.1016/j.mee.2005.07.035).
21. A. Christou, "Solid phase formation in Au: Ge/Ni, Ag/In/Ge, In/Au: Ge GaAs ohmic contact systems," *Solid-State Electronics* **22**, No. 2, 141 (1979). DOI: [10.1016/0038-1101\(79\)90106-0](https://doi.org/10.1016/0038-1101(79)90106-0).
22. V. S. Dmitriev, E. Ya. Shvets, L. B. Dmitrieva, "Technological feature of fabrication of contact to GaAs," *Scientific Bulletin of KUEITM 'New Technologies'*, No. 1-2, 48 (2013).
23. A. V. Murel, V. M. Daniltsev, E. V. Demidov, M. N. Drozdov, V. I. Shashkin, "Effect of rapid thermal annealing on the parameters of gallium-arsenide low-barrier diodes with near-surface δ -doping," *Semiconductors* **47**, No. 11, 1470 (2013). DOI: [10.1134/S106378261311016X](https://doi.org/10.1134/S106378261311016X).

24. T. U. Kampen, S. Park, D. R. T. Zahn, "Barrier height engineering of Ag/GaAs(100) Schottky contacts by a thin organic interlayer," *Appl. Surface Sci.* **190**, No. 1-4, 461 (2002). DOI: [10.1016/S0169-4332\(01\)00919-9](https://doi.org/10.1016/S0169-4332(01)00919-9).
25. V. Ya. Niskov, "Measurement of transient resistance of ohmic contacts to thin layers of semiconductors," *Instrum. Exp. Tech.*, No. 1, 235 (1971).
26. V. Ya. Niskov, V. V. Zadde, A. K. Zaitseva, V. I. Streltsova, "Measurement of transient resistance of ohmic contacts to thin layers of semiconductors," *Instrum. Exp. Tech.*, No. 2, 240 (1971).
27. V. Ya. Niskov, G. A. Kubetskiy, "Ohmic contacts resistance to a thin semiconductor layers," *Semiconductors* **4**, No. 9, 1806 (1970).
28. S. M. Sze, K. N. Kwok, *Physics of Semiconductor Devices*, 3rd ed. (Hoboken: Wiley & Sons, Inc., 2006).