

Application of the Maximum Principle for the Circuits Optimization

A. M. Zemliak^{1,2}

¹*National Technical University of Ukraine “Igor Sikorsky Kyiv Polytechnic Institute”, Kyiv, Ukraine*

²*Autonomous University of Puebla, Puebla, Mexico*

ORCID: [0000-0002-9158-1618](https://orcid.org/0000-0002-9158-1618), e-mail: azemliak@yahoo.com

Received in final form May 4, 2017

Abstract—The solution of the problem of computational time reduce during optimization of electronic circuits allows to enhance the development quality. Generalized methodology of the circuits optimization developed before on a basis of optimal control allows to define many different optimization strategies. Definition of Lyapunov’s function of optimization process and its analysis for different strategies allows to compare these strategies from viewpoint of computational burden and select the best of them. At the same time the most grounded approach for the search of optimal development strategy in this statement is Pontryagin maximum principle. But application of this principle for solution of non-linear problems is related to essential complications. In this paper it is obtained the solution of electronic circuit optimization problem during minimal amount possible processor time on a basis of Pontryagin maximum principle in general case of N variables. It is shown that effect studied before for acceleration of the process of optimization coincides the solution on a basis of the maximum principle. This fact is the theoretical explanation of the acceleration effect. From the other hand the principle of maximum can be the basis for development of the algorithm for electronic circuits optimization with minimal processor time cost.

DOI: 10.3103/S073527271706005X

INTRODUCTION

The possibility of processor time reduce which is necessary for approach of minimal point of the goal function of optimization process can be realized by means of reducing of time for the system analysis and enhancing of the optimization procedures. Due to the structure of conductance matrix of the electronic circuit is sparse many conceptions for operation with sparse matrixes are realized successfully [1]. Another approach to decrease of the operations amount for the electronic circuits analysis is related to decomposition idea [2].

Different methods for analog circuits optimization can be classified with two main groups: determined optimization algorithms and stochastic search algorithms. Some drawbacks of classic determined optimization algorithms are requirement of good initial estimate in the parameters space, unsatisfied local minimum appear, which can be achieved, and frequently the requirement of the goal function continuity and differentiability. To overcome the mentioned problems the special methods are used. For example the centering method for definition of the process initial point [3], the methods of geometric programming [4], allowing to obtain the convergence to the global minimum, but from the other hand requiring special statement of the equations of the design problem, which is related with additional complications. Another approach is based on a method of space map [5] whose purpose is to achieve the satisfactory solution with minimal calculation amount for accurate reference estimations by means of large-scale model optimization.

Stochastic search algorithms, such as evolution algorithms, genetic algorithms, genetic programming, and particles swarm optimization, etc. were developed last years [6–14]. Genetic algorithms are used as a subroutine for analog circuits optimization due to their possibility to find satisfactory solution. Special algorithm defined as method of particles swarm optimization is one of evolution algorithms and it compete with genetic algorithms. This method is applied successfully for optimization of the microwave systems [13, 14].

At the same time restatement of the circuits optimization problem without accurate satisfying of Kirchhoff law proposed in [15–17] opens new possibilities in the design problem. The most common problem statement of the problem of analog circuits design is given in [18, 19]. It is shown such approach allows to generalize the statement of the problem of electronic circuits design and obtain many different

REFERENCES

1. A. George, "On block elimination for sparse linear systems," *SIAM J. Numer. Anal.* **11**, No. 3, 585 (1984). DOI: [10.1137/0711050](https://doi.org/10.1137/0711050).
2. A. Ruehli, A. Sangiovanni-Vincentelli, G. Rabbat, "Time analysis of large-scale circuits containing one-way macromodels," *IEEE Trans. Circuits Syst.* **29**, No. 3, 185 (1982). DOI: [10.1109/TCS.1982.1085127](https://doi.org/10.1109/TCS.1982.1085127).
3. G. Stehr, M. Pronath, F. Schenkel, H. Graeb, K. Antreich, "Initial sizing of analog integrated circuits by centering within topology-given implicit specifications," *Proc. of Int. Conf. on Computer Aided Design*, 9–13 Nov. 2003, San Jose, CA, USA (IEEE, 2003), pp. 241–246. DOI: [10.1109/ICCAD.2003.159696](https://doi.org/10.1109/ICCAD.2003.159696).
4. A. K. Ojha, R. R. Ota, "Multi-objective geometric programming problems with cost coefficients as multiple parameters," *Advanced Modelling and Optimization* **15**, No. 3, 815 (2013), <https://camo.ici.ro/journal/vol15/v15c18.pdf>.
5. J. C. Cervantes-Gonzalez, J. E. Rayas-Sanchez, C. A. Lopez, J. R. Camacho-Perez, Z. Brito-Brito, J. L. Chavez-Hurtado, "Space mapping optimization of handset antennas considering EM effects of mobile phone components and human body," *Int. J. RF Microwave Computer-Aided Engineering* **26**, No. 2, 121 (2016). DOI: [10.1002/mmce.20945](https://doi.org/10.1002/mmce.20945).
6. M. R. M. Ridzuan, E. E. Hassan, A. R. Abdullah, N. Bahaman, A. F. A. Kadir, "A new meta heuristic evolutionary programming (NMEP) in optimizing economic energy dispatch," *J. Telecommunication Electronic and Computer Engineering* **8**, No. 2, 35 (2016), <http://journal.utem.edu.my/index.php/jtec/article/view/942>.
7. F. Yengui, L. Labrak, F. Frantz, R. Daviot, N. Abouchi, I. O'Connor, "A hybrid GA-SQP algorithm for analog circuits sizing," *Circuits and Systems* **3**, No. 2, 146 (2012). DOI: [10.4236/cs.2012.32019](https://doi.org/10.4236/cs.2012.32019).
8. M. W. Cohen, M. Aga, T. Weinberg, "Genetic algorithm software system for analog circuit design," *Procedia CIRP* **36**, 17 (2015). DOI: [10.1016/j.procir.2015.01.033](https://doi.org/10.1016/j.procir.2015.01.033).

9. P. Farago, G. Csipkes, D. Csipkes, C. Farago, S. Hintea, "An FPAA approach to adaptive filter design with evolutionary software-driven reconfiguration," *Elektronika ir Elektrotechnika* **20**, No. 5, 89 (2014). DOI: [10.5755/j01.eee.20.5.5579](https://doi.org/10.5755/j01.eee.20.5.5579).
10. A. Srivastava, T. Kachru, D. Sylvester, "Low-power-design space exploration considering process variation using robust optimization," *IEEE Trans. Computer-Aided Design of Integrated Circuits and Systems* **26**, No. 1, 67 (2007). DOI: [10.1109/TCAD.2006.882491](https://doi.org/10.1109/TCAD.2006.882491).
11. P. A. N. Bosman, "On gradients and hybrid evolutionary algorithms for real-valued multiobjective optimization," *IEEE Trans. Evolutionary Computation* **16**, No. 1, 51 (2012). DOI: [10.1109/TEVC.2010.2051445](https://doi.org/10.1109/TEVC.2010.2051445).
12. M. B. Fallahpour, K. D. Hemmati, A. Pourmohammad, "Optimization of a LNA using genetic algorithm," *Electrical and Electronic Engineering* **2**, No. 2, 38 (2012).
13. M. H. Moradi, M. Abedini, "A combination of genetic algorithm and particle swarm optimization for optimal DG location and sizing in distribution systems," *Int. J. Electrical Power & Energy Systems* **34**, No. 1, 66 (2012). DOI: [10.1016/j.ijepes.2011.08.023](https://doi.org/10.1016/j.ijepes.2011.08.023).
14. A. Engelbrecht, "Particle swarm optimization," *Proc. of 2014 Annual Conf. on Genetic and Evolutionary Computation* (2014), pp. 381–406. DOI: [10.1145/2598394.2605342](https://doi.org/10.1145/2598394.2605342).
15. I. S. Kashirskiy and Ya. K. Trokhimenko, *Generalized Optimization of Electronic Circuits* [in Russian] (Tekhnika, Kiev, 1979).
16. V. Rizzoli, A. Costanzo, C. Cecchetti, "Numerical optimization of broadband nonlinear microwave circuits," *IEEE MTT-S Int. Symp. Dig.*, 8–10 May 1990, Dallas, TX, USA (IEEE, 1990), Vol. 1, pp. 335–338. DOI: [10.1109/MWSYM.1990.99588](https://doi.org/10.1109/MWSYM.1990.99588).
17. E. S. Ochotta, R. A. Rutenbar, L. R. Carley, "Synthesis of high-performance analog circuits in ASTRX/OBLX," *IEEE Trans. Computer-Aided Design of Integrated Circuits and Systems* **15**, No. 3, 273 (1996). DOI: [10.1109/43.489099](https://doi.org/10.1109/43.489099).
18. A. M. Zemliak, "Analog system design problem formulation by optimum control theory," *IEICE Trans. Fund. Elec., Commun. Computer Sci.* **E84-A**, No. 8, 2029 (2001), http://search.ieice.org/bin/summary.php?id=e84-a_8_2029.
19. A. Zemliak, "Analog circuit optimization on basis of control theory approach," *COMPEL: The International Journal for Computation and Mathematics in Electrical and Electronic Engineering* **33**, No. 6, 2180 (2014). DOI: [10.1108/COMPEL-10-2013-0324](https://doi.org/10.1108/COMPEL-10-2013-0324).
20. A. Zemliak, T. Markina, "Behavior of Lyapunov's function for different strategies of circuit optimisation," *Int. J. Electronics* **102**, No. 4, 619 (2015). DOI: [10.1080/00207217.2014.936046](https://doi.org/10.1080/00207217.2014.936046).
21. L. S. Pontryagin, V. G. Boltyanskiy, R. V. Gamkrelidze, E. F. Mischenko, *Mathematical Theory of Optimal Process* [in Russian] (Nauka, Moscow, 1969).
22. J. B. Rosen, "Iterative solution of nonlinear optimal control problems," *SIAM J. Control* **4**, No. 1, 223 (1966). DOI: [10.1137/0304021](https://doi.org/10.1137/0304021).
23. R. P. Fedorenko, *Approximate Solution of Optimal Control Problems* [in Russian] (Nauka, Moscow, 1978).
24. L. Bourdin, E. Trelat, "Pontryagin maximum principle for finite dimensional nonlinear optimal control problems on time scales," *SIAM J. Control Optim.* **51**, No. 5, 3781 (2013). DOI: [10.1137/130912219](https://doi.org/10.1137/130912219).
25. R. Kamocki, "Pontryagin maximum principle for fractional ordinary optimal control problems," *Mathematical Methods in the Applied Sciences* **37**, No. 11, 1668 (2014). DOI: [10.1002/mma.2928](https://doi.org/10.1002/mma.2928).
26. A. M. Zemliak, "Application of Pontryagin maximum for the circuits optimization problem," *Proc. of VII All-Russian Sci. and Technol. Conf. on Problems of Advanced Micro- and Nanoelectronic Systems Development*, 2016 (2016), Vol. 1, pp. 94–100.